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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/613,177	07/03/2003	Stefan Marinca	T0461.70040 US00	6560
23628 7:	590 06/15/2006		EXAMINER	
WOLF GREENFIELD & SACKS, PC			PATEL, RAJNIKANT B	
FEDERAL RE	SERVE PLAZA		1221212	0.000.000
600 ATLANTIC AVENUE			ART UNIT	PAPER NUMBER
BOSTON, MA 02210-2206			2838	

DATE MAILED: 06/15/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

SUPPLEMENTAL	Application No.	Applicant(s)			
	10/613,177	MARINCA, STEFAN			
Notice of Allowability	Examiner	Art Unit			
	Rajnikant B Patel	2838			
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication (GHTS). This application is subject to	plication. If not included will be mailed in due course. THIS			
1. This communication is responsive to 15 July 2005.					
2. The allowed claim(s) is/are <u>1-28</u> .		•			
3. The drawings filed on are accepted by the Examiner.					
4. ☐ Acknowledgment is made of a claim for foreign priority una) ☐ All b) ☐ Some* c) ☐ None of the:  1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:	been received. been received in Application No cuments have been received in this	national stage application from the			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	IENT of this application.	•			
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give					
<ul> <li>6.  ☐ CORRECTED DRAWINGS ( as "replacement sheets") must</li> <li>(a) ☐ including changes required by the Notice of Draftspers</li> <li>1) ☐ hereto or 2) ☐ to Paper No./Mail Date</li> <li>(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date</li> <li>Identifying indicia such as the application number (see 37 CFR 1)</li> </ul>	on's Patent Drawing Review (PTO s Amendment / Comment or in the C .84(c)) should be written on the drawin	office action of			
each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).  7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.					
<b>*</b>					
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 3/16/05</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	6. Interview Summary Paper No./Mail Dat 08), 7. Examiner's Amendr	te			

## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 1-28 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

Applicant invention provides an improved reference source. The reference source has reduced sensitivity to the input offset voltage of the amplifier components in the reference circuit. This achieved by subtracting two currents at the reference output node such that the combined offset sensitivity is less than the corresponding offset sensitivity for only one current. The first bipolar transistor circuit includes a stacked arrangement of transistors, the first control circuit includes an amplifier, stacked arrangement of transistors being coupled to the non-inverting input of the amplifier being coupled to current mirror to provide the current to the current source, the second bipolar transistor circuit is coupled to a non-inverting input of the amplifier component of the second control circuit, the output of the amplifier component controlling the gate of MOSFET transistor to provide the current provided by the current sink. None of the cited art overcomes the claim limitation of the instant application since claims are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably Application/Control Number: 10/613,177 Page 3

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rajnikant B Patel whose telephone number is 571-272-2082. The examiner can normally be reached on 6.30-5.00; m-f.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

> Primary Examiner Art Unit 2838